

WHAT IS CLAIMED IS:

1. A circuit comprising:

a plurality of trim tracking links coupled in series;

5 a plurality of detecting devices wherein each detecting device is coupled in parallel with a corresponding trim tracking link; and

10 a probe pad coupled to the string of trim tracking links.

15 2. The circuit of claim 1 wherein the detecting devices are diodes.

20 3. The circuit of claim 1 wherein the detecting devices are resistors.

25 4. The circuit of claim 1 further comprising a calibration diode device coupled in parallel with the plurality of trim tracking links.

30 5. The circuit of claim 4 wherein the calibration diode device comprises at least one diode.

35 6. The circuit of claim 4 wherein the calibration diode device comprises a plurality of diodes coupled in series, and coupled in parallel with the plurality of trim tracking links.

7. The circuit of claim 4 wherein the detecting devices are diodes.

5 8. The circuit of claim 4 wherein the detecting devices are resistors.

9. A circuit for detecting and tracking a status of a  
10 device under laser trim comprising:

15 a series connected string of trim tracking links; and  
a plurality of detecting devices wherein each detecting device is coupled in parallel with a corresponding trim tracking link.

10. The circuit of claim 9 wherein the detecting devices are diodes.

11. The circuit of claim 9 wherein the detecting devices  
20 are resistors.

12. The circuit of claim 9 further comprising a calibration diode device coupled in parallel with the series connected string of trim tracking links.

13. The circuit of claim 9 further comprising a plurality of diodes coupled in series, and coupled in parallel with the series connected string of trim tracking links.

5 14. The circuit of claim 13 wherein the detecting devices are diodes.

15. The circuit of claim 13 wherein the detecting devices are resistors.

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16. A circuit for detecting and tracking a status of a device under laser trim comprising:

at least two trim tracking links coupled in series; and

at least two trim tracking diodes wherein each trim

15 tracking diode is coupled in parallel with a corresponding trim tracking link; and

a calibration diode device coupled in parallel with the at least two trim tracking links.

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17. The circuit of claim 16 wherein the calibration diode device comprises at least one diode.

25 18. The circuit of claim 16 wherein the calibration diode device comprises a plurality of calibration diodes coupled in series, and coupled in parallel with the at least two trim tracking links.